

University of Kerala

Fifth Semester B.Tech (PTR) Practical Examination January 2025

(2008 Scheme)

Time Table

Branch: Electronics and Communication Engineering

Examination Centre: University College of Engineering, Kariavattom

Subject	Date of Examination	Time
PT10.505- Digital Signal Processing Lab	04.06.2025 Wednesday	09.30 am to 12.30

Sd/-

Controller of Examinations